

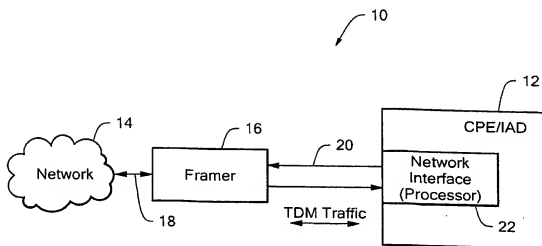


Application No. 10/723,804

Borkowski et al.

CYCLE-EFFICIENT TDM TIMESLOT REMAPPING FOR NETWORK PROCESSING ENGINES  
Replacement Sheet

1/6



**FIG. 1**

2/6

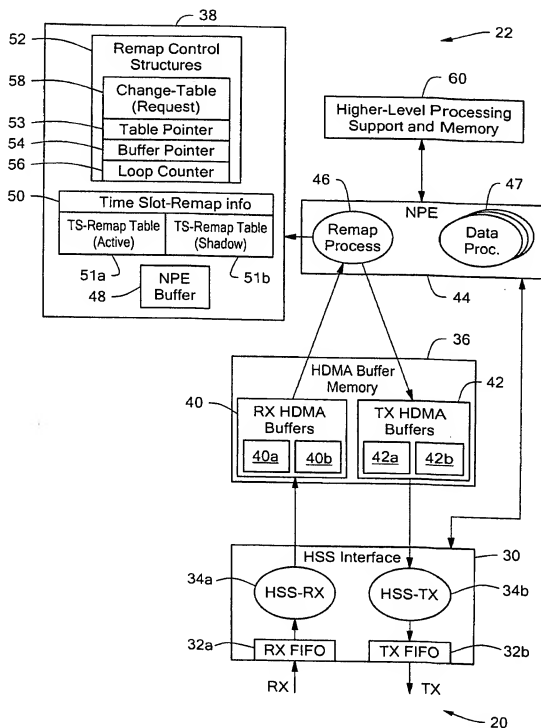
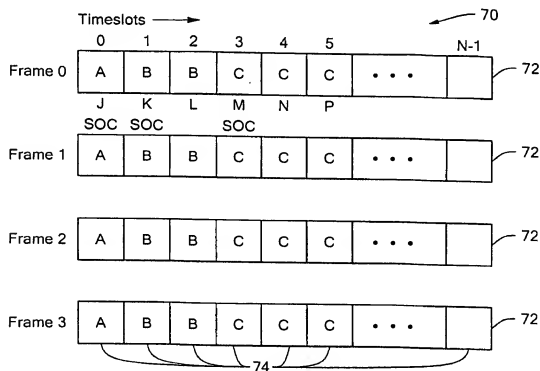
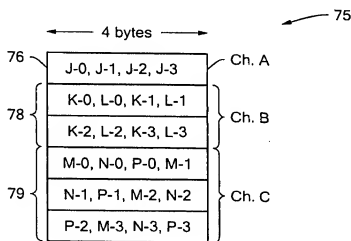


FIG. 2

3/6

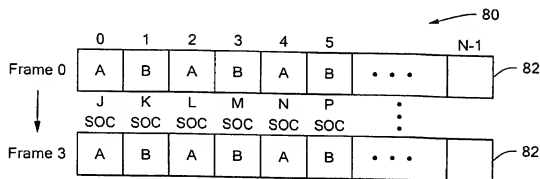


**FIG. 3A**

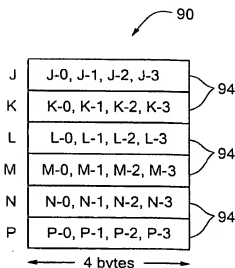


**FIG. 3B**

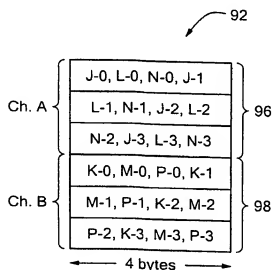
4/6



**FIG. 4A**



**FIG. 4B**



**FIG. 4C**

5/6

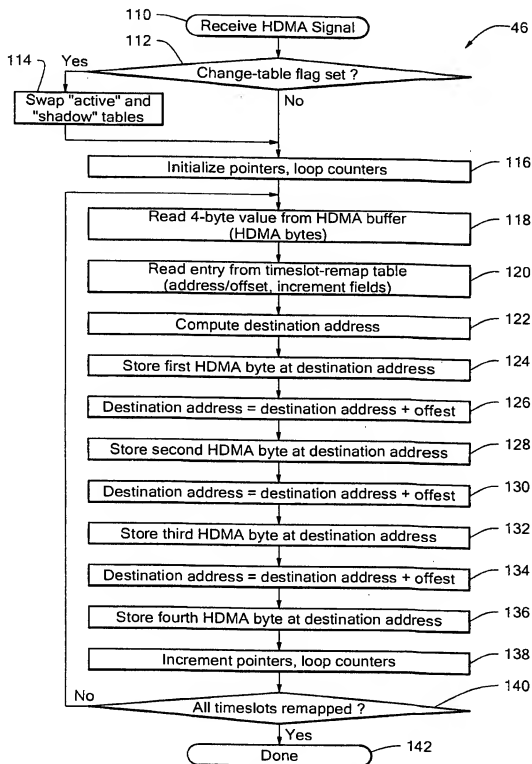
51a

Timeslots

0	Destination Address/Offset	Increment value/Offset	100
1			100
2			100
3			•
4			•
•			•
•			
•			
N-1			100

**FIG. 5**

6/6



**FIG. 6**